

FIG. 1

DECIMAL	BINARY
0	000000
1	000001
2	000010
4	000100
5	000101
8	001000
9	001001
10	001010
16	010000
17	010001
18	010010
20	010100
21	010101
32	100000
33	100001
34	100010
36	100100
37	100101
40	101000
41	101001
42	101010

00000305 112101
101214 566060

TOTET-56268660

FIG. 2

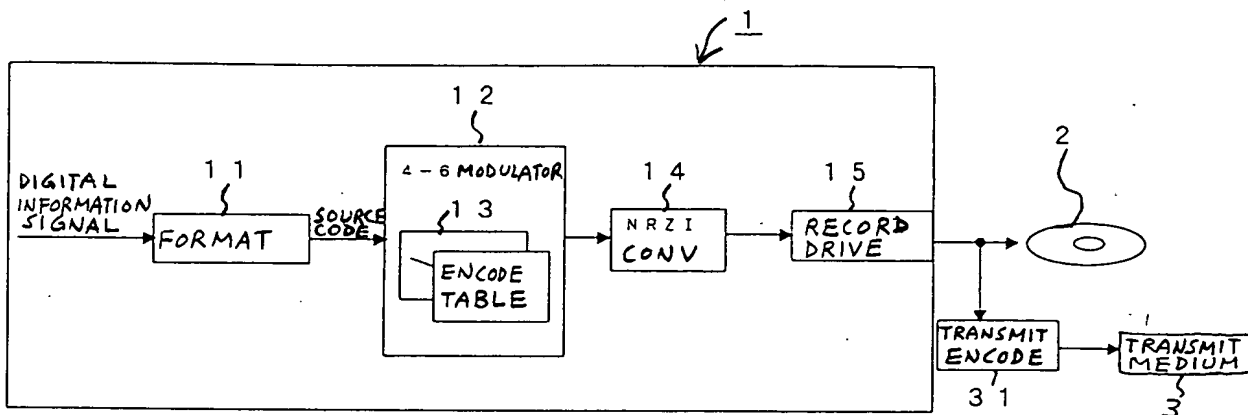
S(k)	0			1			2			3		
	D(k)	C(k)	S(k+1)	C(k)	S(k+1)		C(k)	S(k+1)		C(k)	S(k+1)	
0	0	1	000001	0	9	001001	0	33	100001	0	41	101001
1	1	1	000001	1	9	001001	1	33	100001	1	41	101001
2	2	17	010001	0	5	000101	0	17	010001	0	37	100101
3	3	17	010001	1	5	000101	1	17	010001	1	37	100101
4	4	18	010010	1	2	000010	1	18	010010	1	34	100010
5	5	18	010010	2	2	000010	2	18	010010	2	34	100010
6	6	18	010010	3	2	000010	3	18	010010	3	34	100010
7	7	21	010101	0	4	000100	1	36	100100	1	21	010101
8	8	21	010101	1	4	000100	2	36	100100	2	21	010101
9	9	20	010100	1	4	000100	3	36	100100	3	20	010100
10	10	20	010100	2	10	001010	1	42	101010	1	20	010100
11	11	20	010100	3	8	001000	1	40	101000	1	20	010100
12	12	0	000000	2	10	001010	2	42	101010	2	32	100000
13	13	0	000000	3	10	001010	3	42	101010	3	32	100000
14	14	16	010000	2	8	001000	2	40	101000	2	16	010000
15	15	16	010000	3	8	001000	3	40	101000	3	16	010000

TABLE 5666666

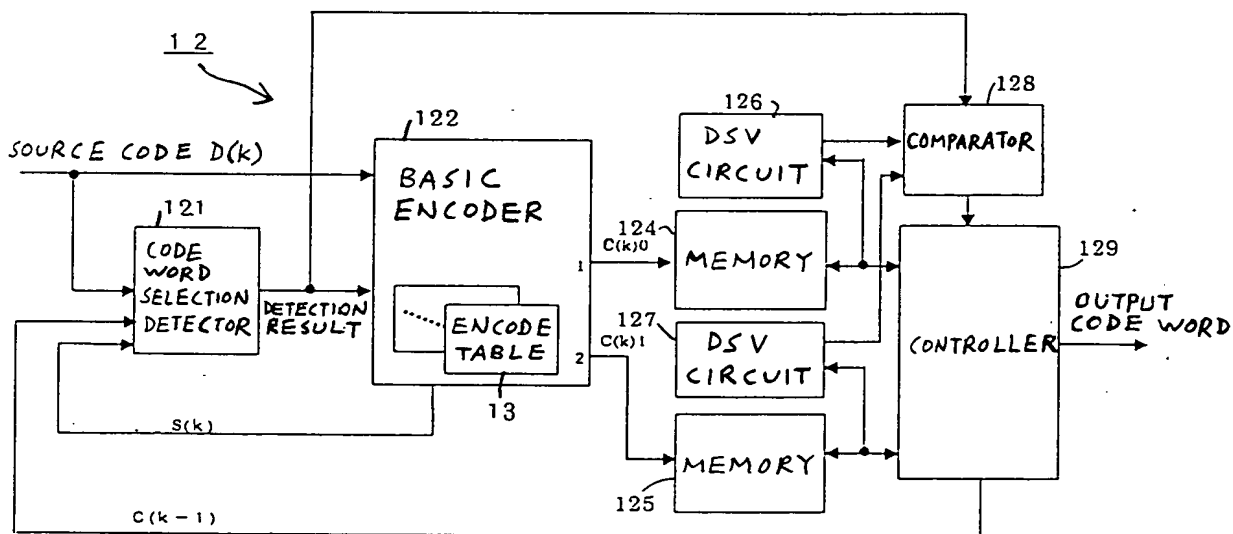
FIG. 3

S(k)		0				1				2				3			
D(k)		C(k)		S(k+1)		C(k)		S(k+1)		C(k)		S(k+1)		C(k)		S(k+1)	
0	1	000001	0	9	001001	0	33	100001	0	41	101001	0	41	101001	0	41	101001
1	17	010001	1	5	000101	1	17	010001	1	37	100101	1	37	100101	1	37	100101
2	18	010010	2	2	000010	2	18	010010	2	34	100010	2	34	100010	2	34	100010
3	17	010001	0	5	000101	0	17	010001	0	37	100101	0	37	100101	0	37	100101
4	18	010010	1	2	000010	1	18	010010	1	34	100010	1	34	100010	1	34	100010
5	1	000001	1	9	001001	1	33	100001	1	41	101001	1	41	101001	1	41	101001
6	18	010010	3	2	000010	3	18	010010	3	34	100010	3	34	100010	3	34	100010
7	20	010100	1	4	000100	1	36	100100	1	20	010100	1	20	010100	1	20	010100
8	21	010101	0	4	000100	2	36	100100	2	21	010101	2	21	010101	2	21	010101
9	20	010100	2	4	000100	3	36	100100	3	20	010100	3	20	010100	3	20	010100
10	21	010101	1	10	001010	1	42	101010	1	21	010101	1	21	010101	1	21	010101
11	20	010100	3	8	001000	1	40	101000	1	20	010100	1	20	010100	1	20	010100
12	16	010000	2	8	001000	2	40	101000	2	16	010000	2	16	010000	2	16	010000
13	0	000000	3	10	001010	3	42	101010	3	32	100000	3	32	100000	3	32	100000
14	16	010000	3	8	001000	3	40	101000	3	16	010000	3	16	010000	3	16	010000
15	0	000000	2	10	001010	2	42	101010	2	32	100000	2	32	100000	2	32	100000

FIG. 4



F I G. 5



START

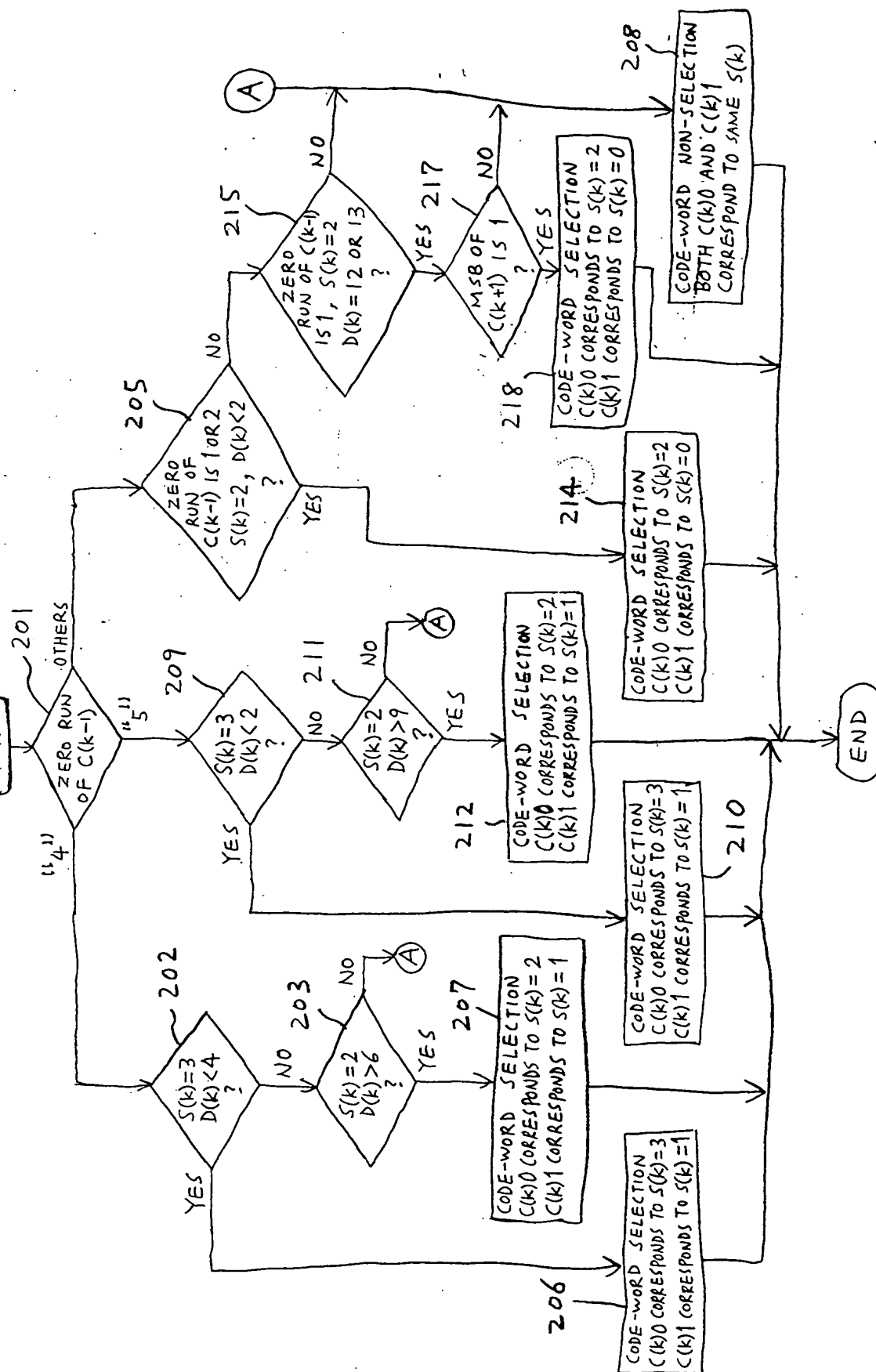


FIG. 10

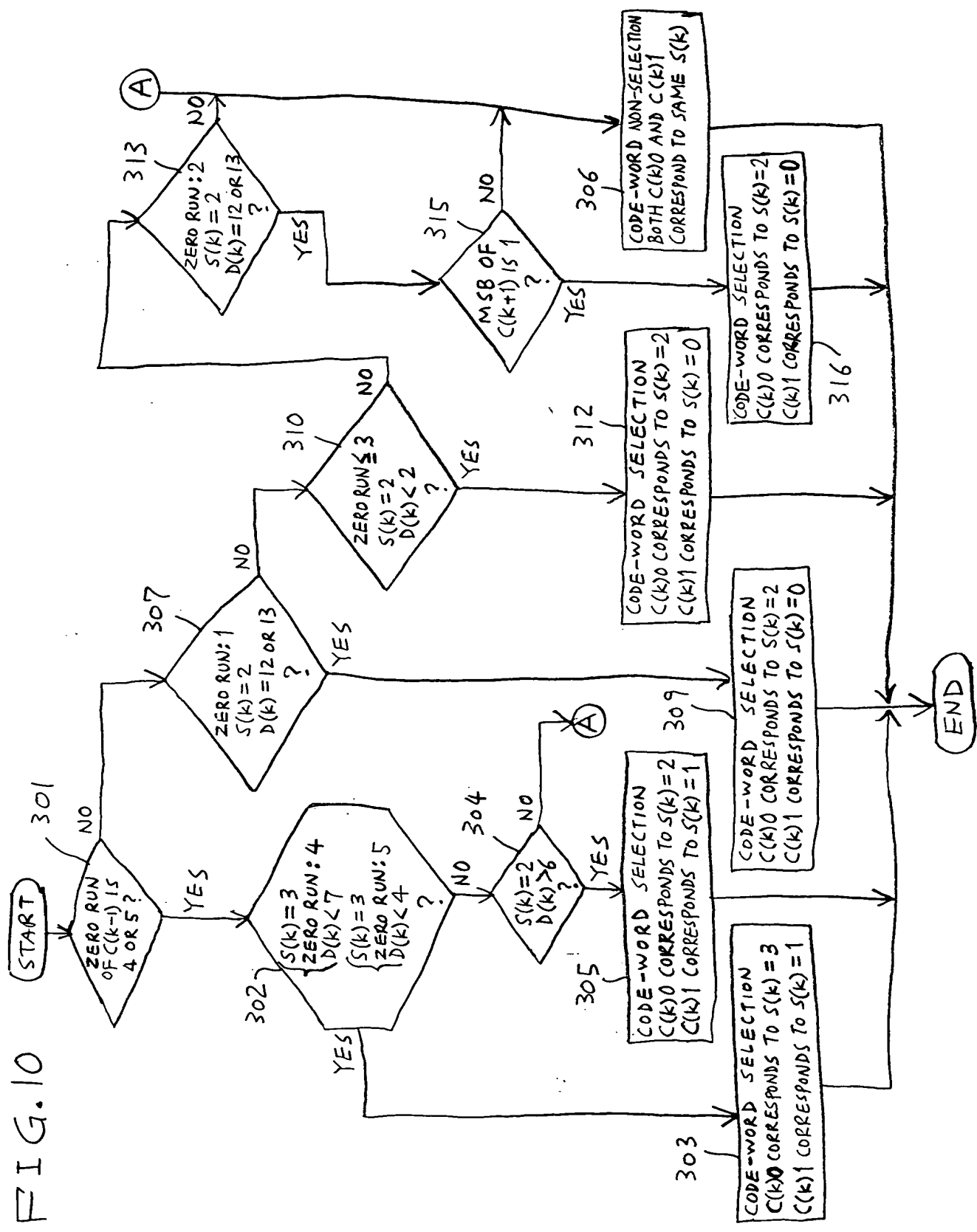


FIG. 11

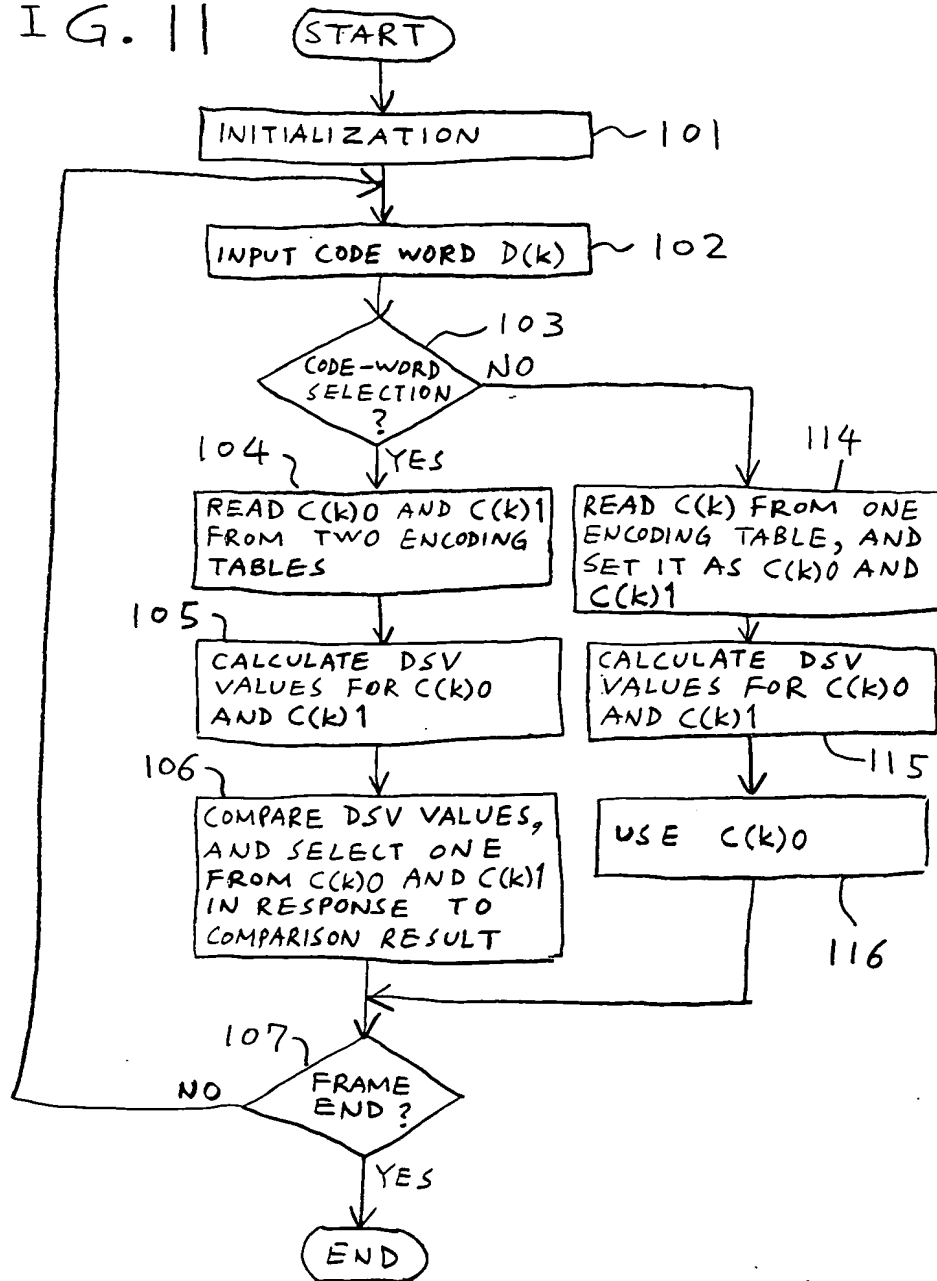


FIG. 11


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graph TD
    Dk[D(k)] --> 131[MIN RUN REPETITION MONITOR 131]
    131 --> 132[SELECTION DETECTING CIRCUIT 132]
    130[130 MAX RUN SETTING CIRCUIT] --> 132
    132 --> 135[135 ADDRESS CALCULATION CIRCUIT]
    135 --> 13[ENCODING TABLE 13]
    135 -- S(k) --> 136[1-WORD DELAY 136]
    136 -- S(k+1) --> 132
    13 --> 137[DISTRIBUTOR 137]
    137 --> Ck0[C(k)0]
    137 --> Ck1[C(k)1]
  
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FIG. 13

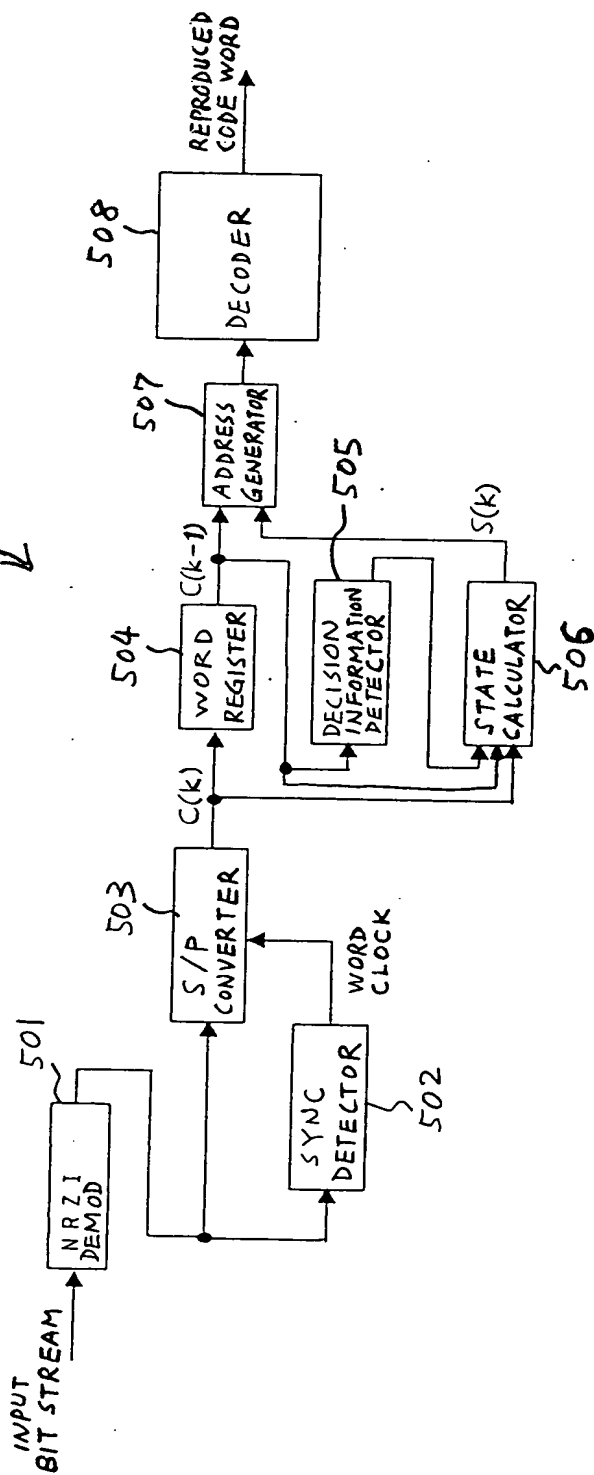


FIG. 14

C(k-1)		DECISION INFORMATION	D(k-1)			
DECIMAL	BINARY		S(k)=0	S(k)=1	S(k)=2	S(k)=3
0	000000	2	7	-	12	13
1	000001	0	0	1	-	-
2	000010	1	-	4	5	6
4	000100	1	-	7	8	9
5	000101	0	2	3	-	-
8	001000	1	-	11	14	15
9	001001	0	0	1	-	-
10	001010	1	-	10	12	13
16	010000	2	-	-	14	15
17	010001	0	2	3	-	-
18	010010	1	-	4	5	6
20	010100	1	-	9	10	11
21	010101	0	7	8	-	-
32	100000	2	-	-	12	13
33	100001	0	0	1	-	-
34	100010	1	-	4	5	6
37	100101	0	2	3	-	-
40	101000	1	-	11	14	15
41	101001	0	7	8	-	-
42	101010	1	-	10	12	13

FIG. 15

D(k)	C(k)	DECISION INFORMATION	S(k)
1 5	0 1 0 0 0 0	2	3
0	0 0 1 0 0 1	0	0
1	0 0 0 0 0 1	0	1
2	0 0 0 1 0 1	0	0
3	0 1 0 0 0 1	0	—